



優潔光科技

UJ Light Technologies

**SPECIFICATIONS FOR
LCD MODULE**

Model No:

UTF088A-KFN\$

UJ Light Technologies Corporation

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1. General Specification

Item	Specification	Unit
LCD Mode	a-Si TFT/transmissive	
Screen Size(inch)	8.8"	
Color	16.7M	
Display Resolution	480*3(RGB)*1920	pixels
Viewing Direction	All	O' Clock
Outline Dimension	64.0(W)*231.0(H)*6.1(T)	mm
Active Area(A.A)	54.72*(W)*218.88(H)	mm
Display Mode	Normally Black	
Pixel Arrangement	RGB-stripe	
Interface	MIPI	
Backlight	White LED	

2. Absolute Maximum Rating

Item	Symbol	Min.	Typ.	Max.	Unit
Power Supply Voltage	VGH	17.0	18.0	19.0	V
Power Supply Voltage	VGL	-11.0	-10.0	-9.0	V
Current of power supply	IGH	-	-	5.0	mA
Current of power supply	IGL	-	-	-5.0	mA
Operating Temperature	TOP	-20	-	70	°C
Storage Temperature	TST	-30	-	80	°C



3. Electrical Characteristics

3.1 TFT LCD Electrical Characteristics

Item	Symbol	Min	Typ	Max	Unit
Power Supply Voltage	VDD	3.0	3.3	3.6	V
Power Supply Voltage	AVDD	11.8	12	12.2	V
VCOM	VCOM	4.5	4.88	5.2	V
Input signal voltage	VIH	0.7VDD	-	VDD	V
Input signal voltage	VIL	0	-	0.3VDD	V
Current of power supply	IDD	-	-	35	mA
Current of power supply	IADD	-	-	30	mA
Current of power supply	Ivcom	-	-	0.1	mA

3.2 BackLight Characteristics

Item	Symbol	Min.	Typ.	Max.	Unit
Supply Voltage	VF	13.5	15.0	16.5	V
Supply Current	IF	-	180	-	mA
Brightness	IV	700	800		cd/m ²
Uniformity	B	80			%
Color	White				



4. Interface Pin Function

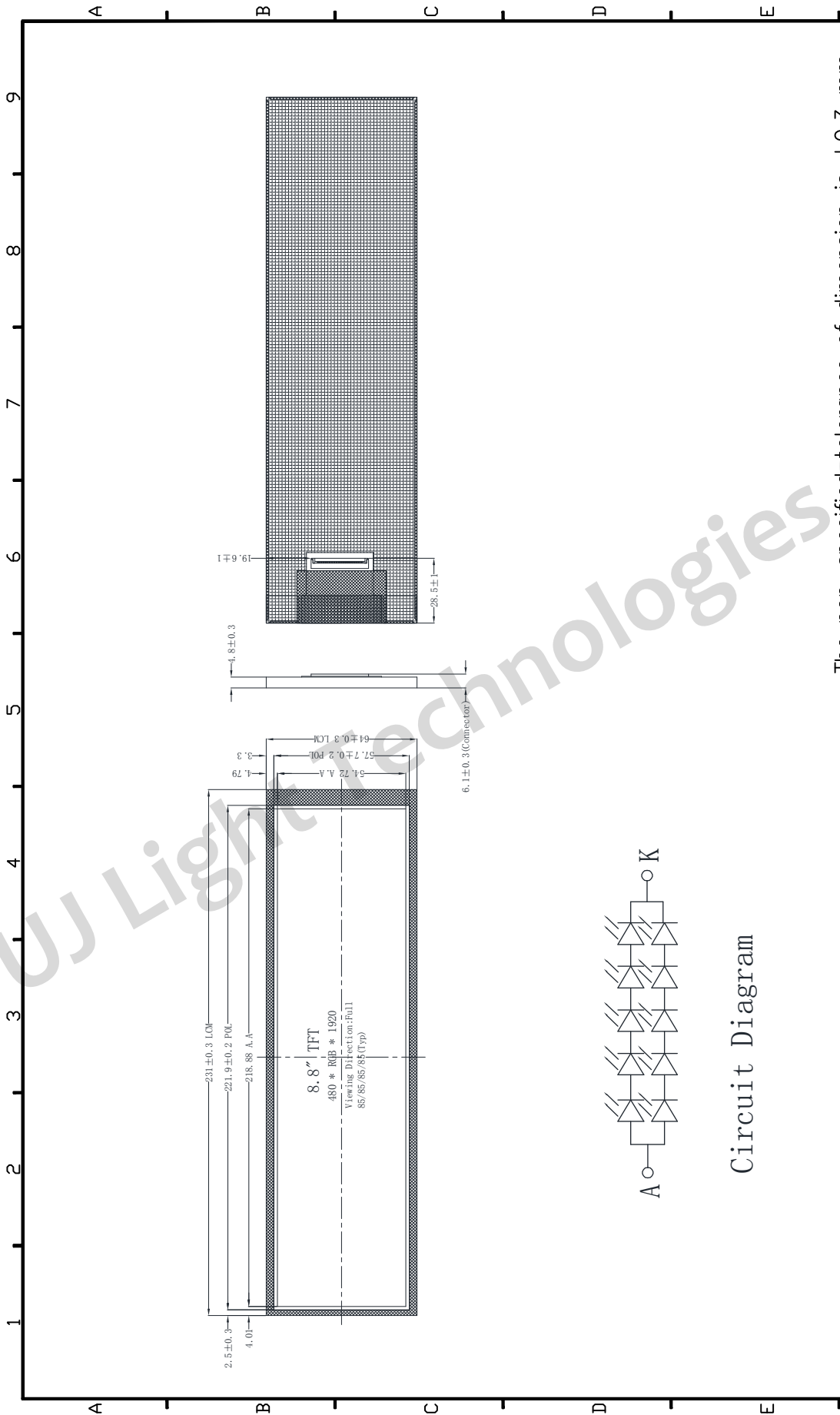
Pin NO.	Symbol	Description
1	GND	Ground
2	NC	No connection
3	LED+	LED Anode
4	LED+	LED Anode
5	NC	No connection
6	LED-	LED Cathode
7	LED-	LED Cathode
8	NC	No connection
9	GND	Ground
10	NC	No connection
11	AVDD	Power supply for analog circuit
12	NC	No connection
13	VGH	Power supply for analog circuit
14	NC	No connection
15	VGL	Power supply for analog circuit
16	NC	No connection
17	GND	Ground
18	VCOM	Power supply for common voltage
19	GND	Ground
20	GND	Ground
21	RESET	Global reset
22	VDD	Power supply for digital circuits
23	STBYB	Standby mode
24	TP_Sync	Sync signal for touch panel
25	GND	Ground
26	D0P	MIPI Data Input Lane0 positive-end
27	D0N	MIPI Data Input Lane0 negative-end
28	GND	Ground
29	D1P	MIPI Data Input Lane1 positive-end
30	D1N	MIPI Data Input Lane1 negative-end
31	GND	Ground
32	CLKP	MIPI Clock Input positive-end
33	CLKN	MIPI Clock Input negative-end
34	GND	Ground
35	D2P	MIPI Data Input Lane2 positive-end
36	D2N	MIPI Data Input Lane2 negative-end



37	GND	Ground
38	D3P	MIPI Data Input Lane3 positive-end
39	D3N	MIPI Data Input Lane3 negative-end
40	GND	Ground

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5. Drawing



Circuit Diagram

The non-specified tolerance of dimension is ±0.3 mm

TOLERANCE UNLESS OTHERWISE SPEC.		NO.		REV.		DESCRIPTION		LOCA	DRAW	DATE	PART_NAME		TITLE	
F	G	H	J	K	L						UNIT	SCALE	SHEET	OF
~ 8	' 0.05	' 0.1	' 0.2	' 0.3	' 0.4						m/m	1/1	1	1
8 ~ 25	' 0.08	' 0.15	' 0.3	' 0.3	' 0.6									
25 ~ 80	' 0.12	' 0.2	' 0.25	' 0.4	' 0.5	' 0.8								
80 ~ 250	' 0.25	' 0.3	' 0.4	' 0.6	' 0.8	' 1.2								
250~	' 0.5	' 0.6	' 0.8	' 1.0	' 1.5	' 2.0								
											UJ LIGHT	MATERIAL	NA	
											VERSION	SURFACE	NA	
											APPROVAL / DATE	TOL. UNLESS NOTED.		NA
											CHECK / DATE			NA
											DRAW / DATE			NA